



Atty. Dkt. No. 00AB147 (081696-0235)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Gary Dan Dotson

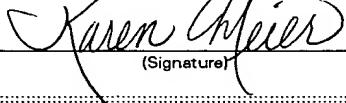
Title: PROGRAMMABLE ERROR  
CHECKING VALUE CIRCUIT  
AND METHOD

Appl. No.: 09/675,855

Filing Date: 09/29/2000

Examiner: Torres, Joseph D.

Art Unit: 2133

<b>CERTIFICATE OF EXPRESS MAILING</b>	
I hereby certify that this correspondence is being deposited with the United States Postal Service's "Express Mail Post Office To Addressee" service under 37 C.F.R. § 1.10 on the date indicated below and is addressed to: Commissioner for Patents, PO Box 1450, Alexandria, Virginia 22313-1450.	
EV 055900661 US (Express Mail Label Number)	5/27/03 (Date of Deposit)
Karen Meier, (Printed Name)	
	
(Signature)	

**RECEIVED**

MAY 30 2003

**TRANSMITTAL OF FORMAL DRAWINGS** Technology Center 2100

**ATTENTION: DRAWING REVIEW BRANCH**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Sir:

Transmitted herewith are the formal drawings (8 sheets, Figures 1-8) for the above-identified application. The Official Draftsperson is respectfully requested to approve these drawings for entry into the application.

Respectfully submitted,

Date 5/27/03

By D. G. Luettgen

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